

Figure 1.



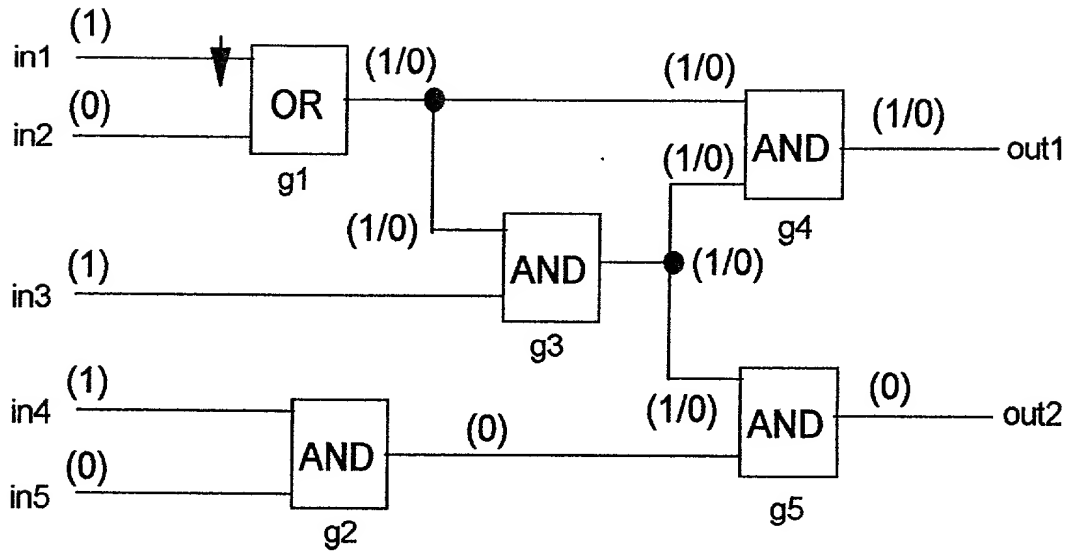


Figure 3.

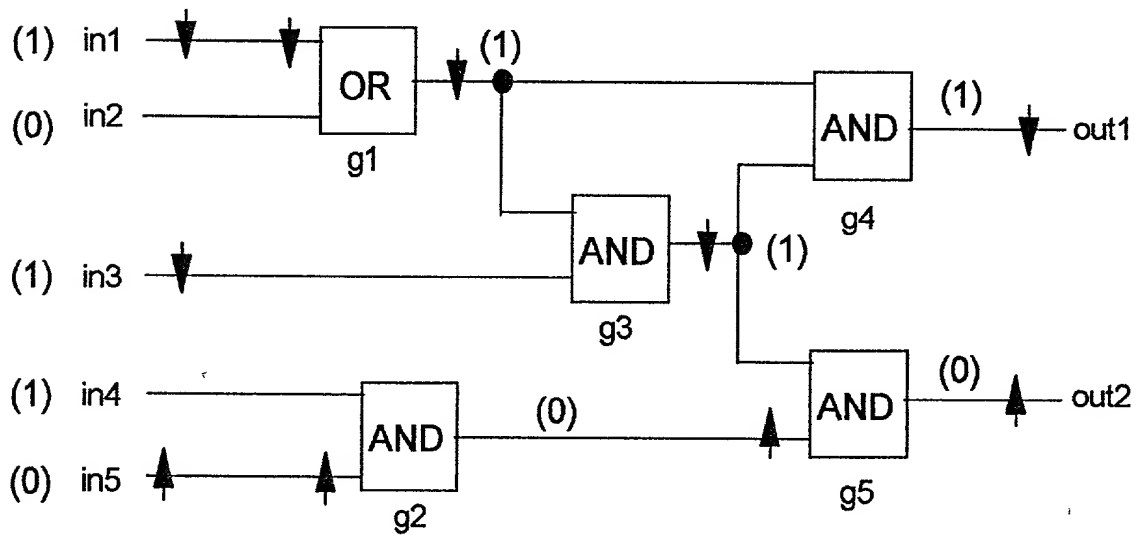


Figure 4.

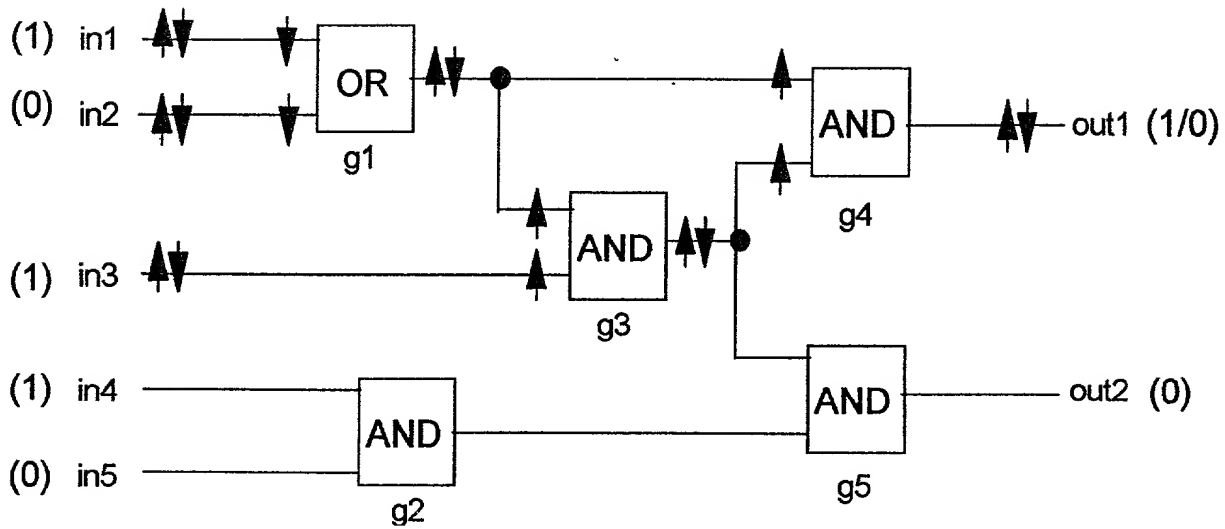


Figure 5

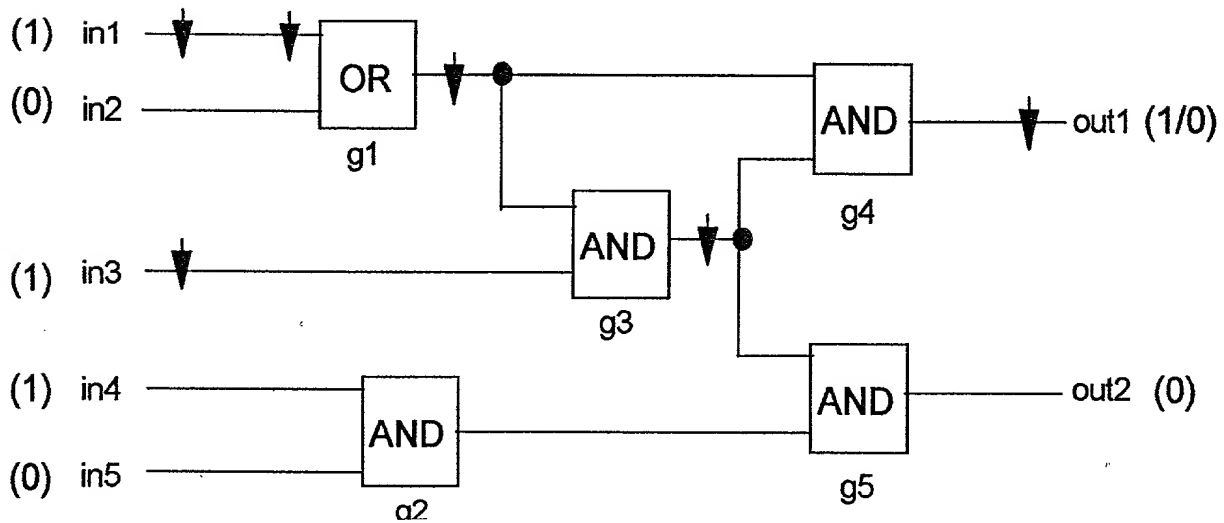
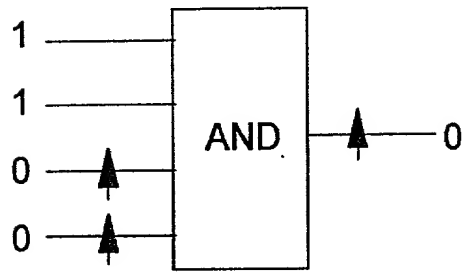
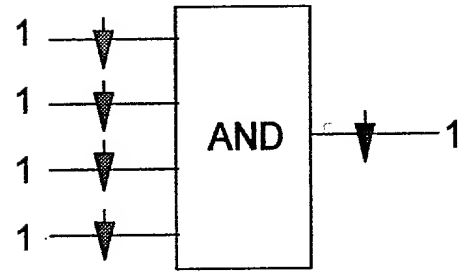


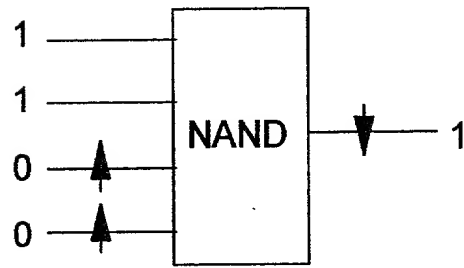
Figure 6.



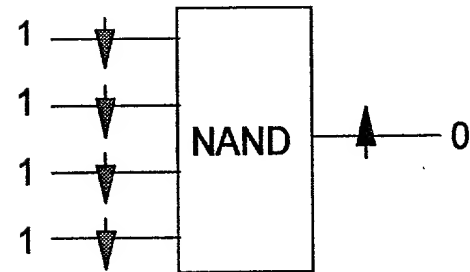
(a)



(b)



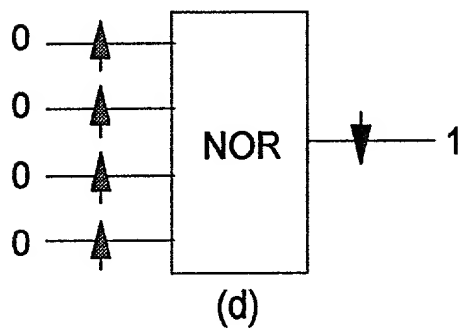
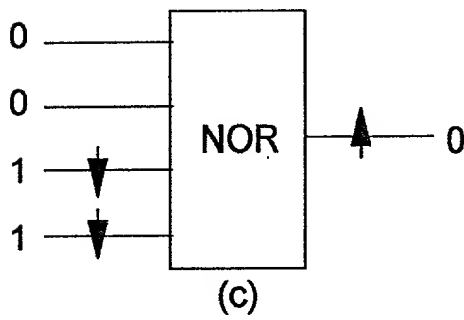
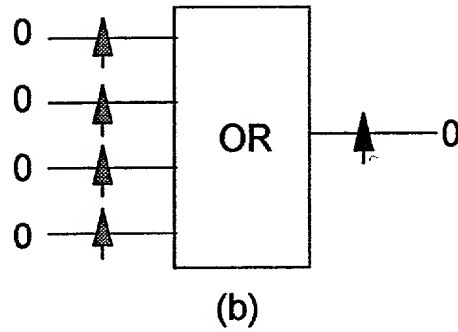
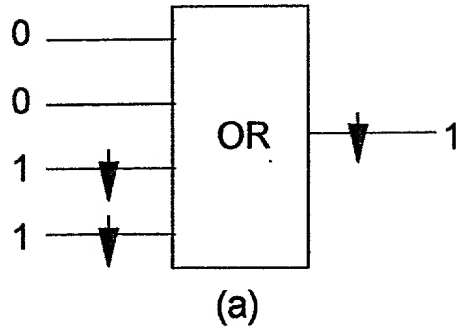
(c)



(d)

↓↑ collapsed stuck-at faults  
↓↑ non-collapsed stuck-at faults

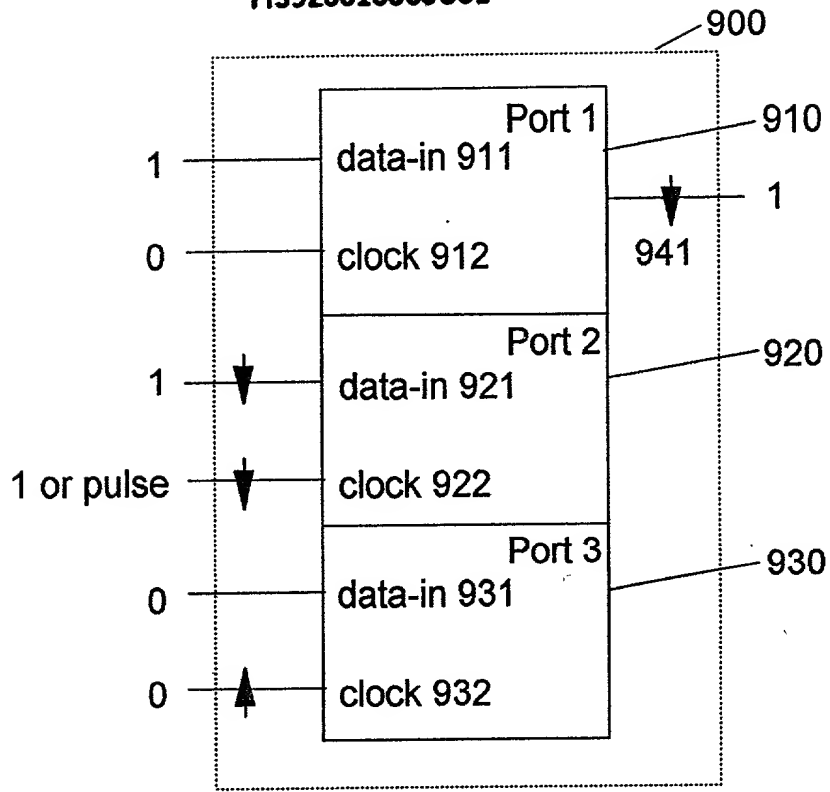
Figure 7



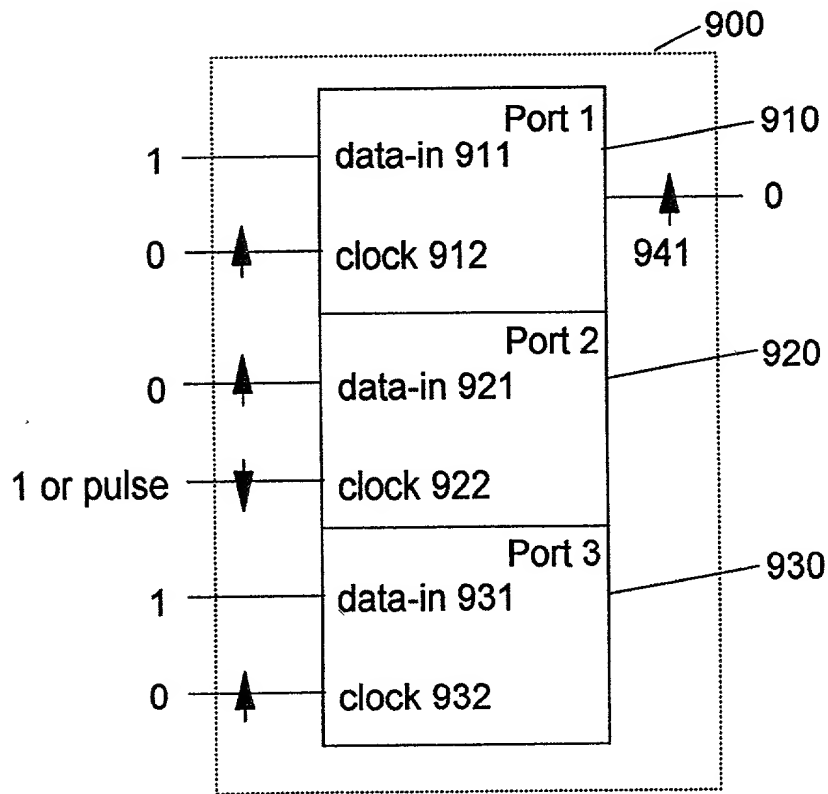
↓↑ collapsed stuck-at faults

↓↑ non-collapsed stuck-at faults

Figure 8.

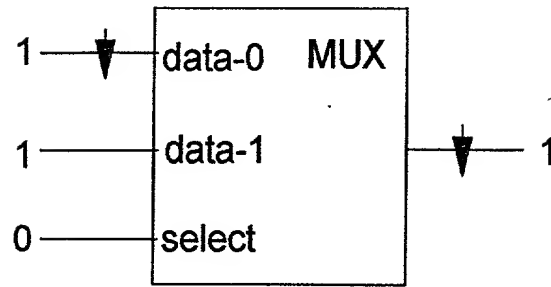


(a)

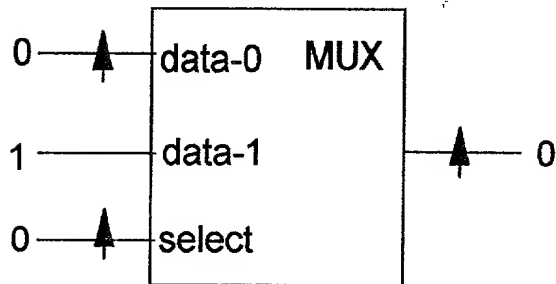


(b)

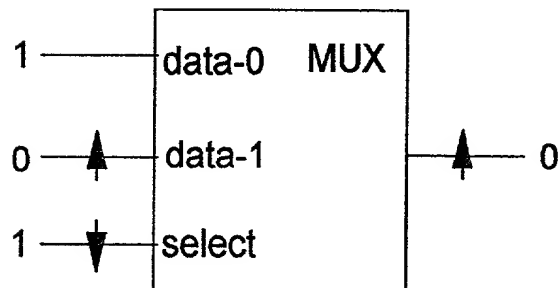
Figure 9.



(a)



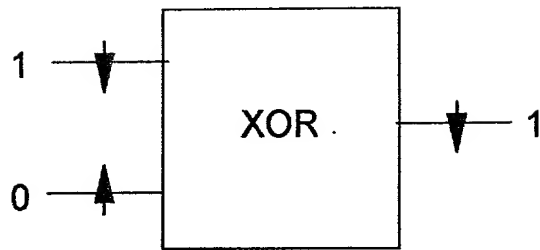
(b)



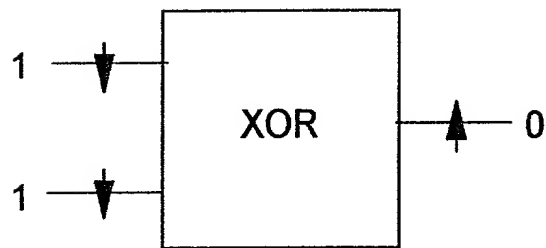
(c)

Figure 10.

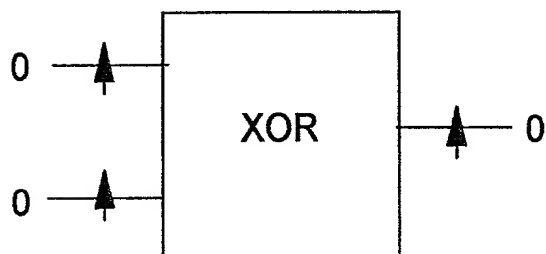




(a)



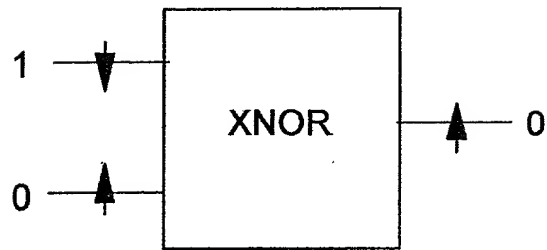
(b)



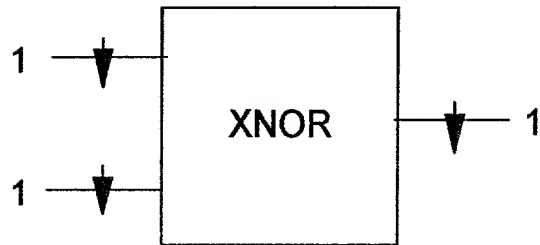
(c)

Figure 11.

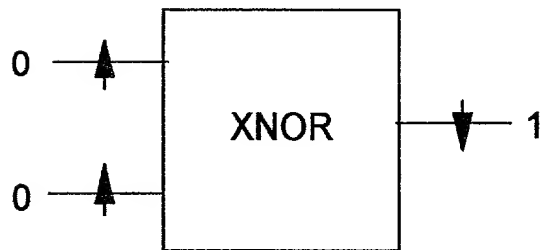
09254-06101  
TOTAL 452660



(a)



(b)



(c)

Figure 12.

0937854-05101  
TOT 90-45382860

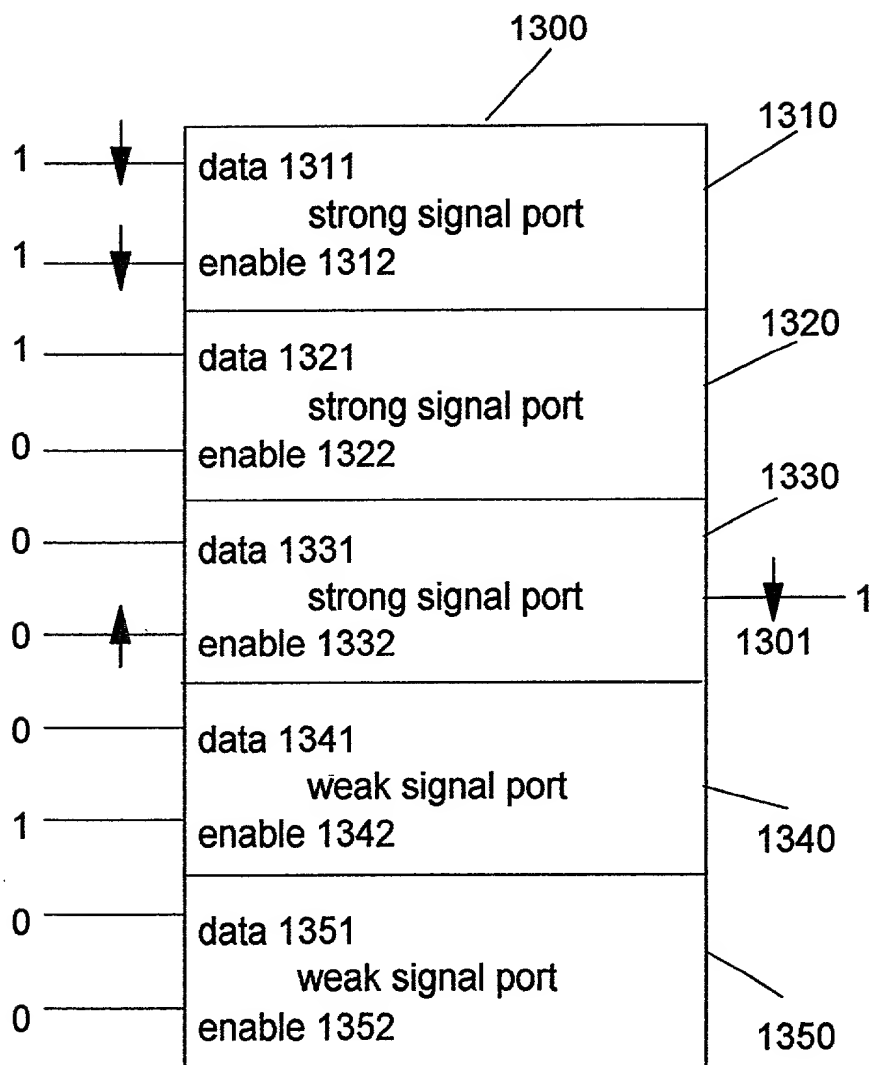


Figure 13.

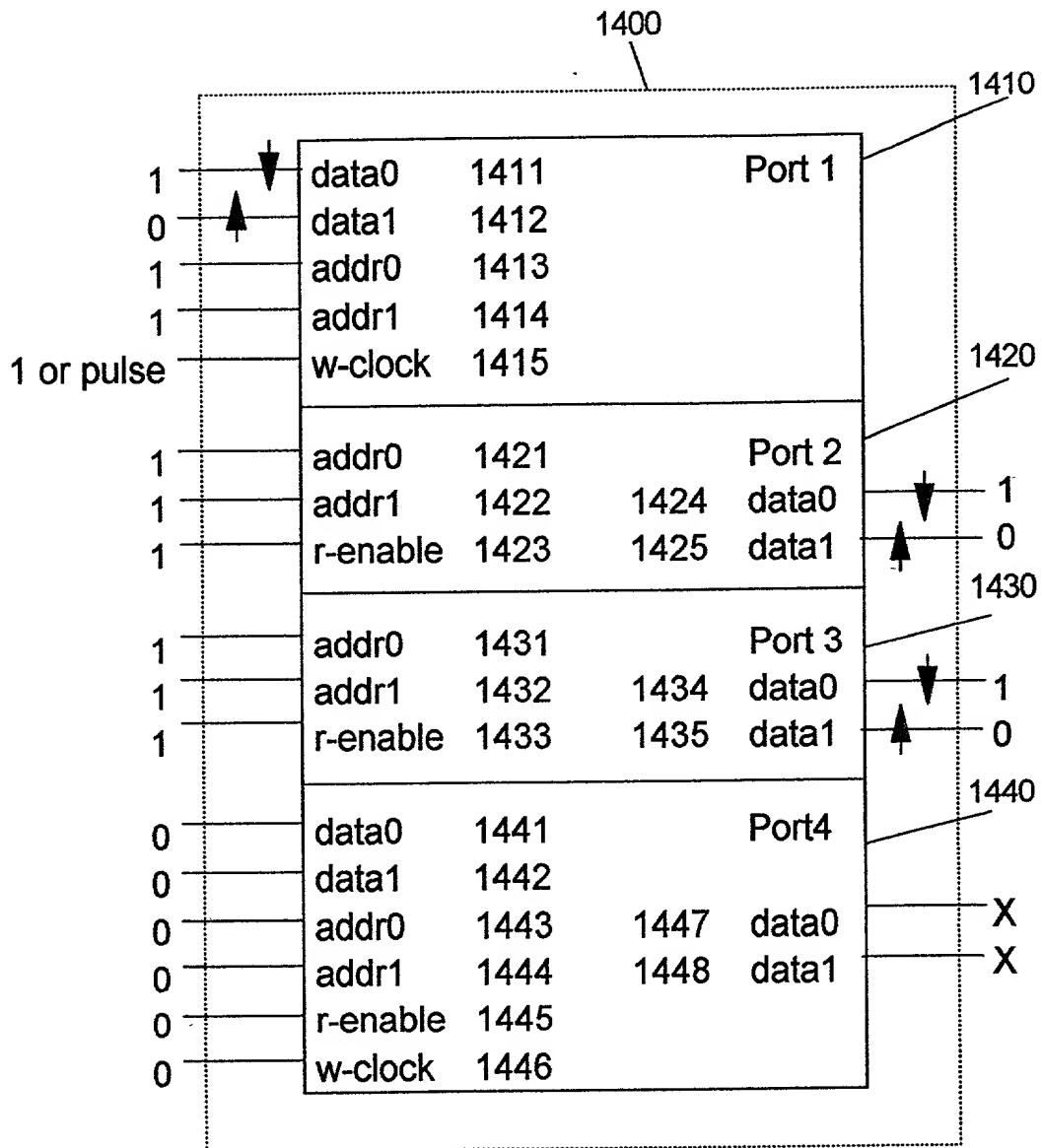


Figure 14.

# ROM CONTENT TABLE

addr0	addr1	bit0	bit1
0	0	0	0
0	1	0	1
1	0	1	0
1	1	1	1

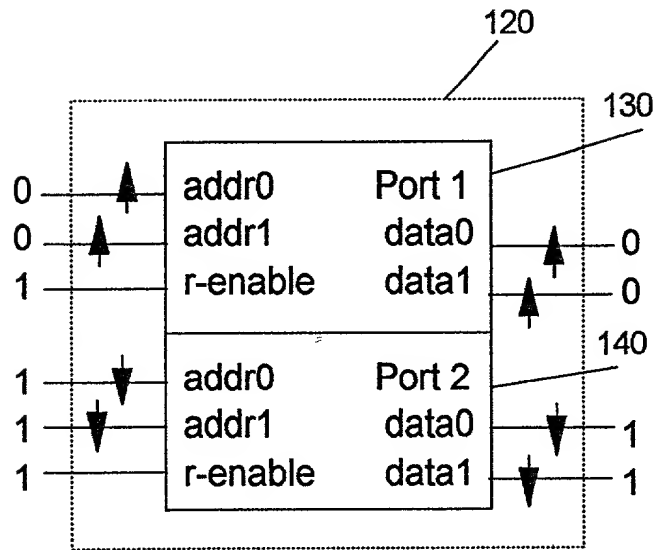


Figure 15.

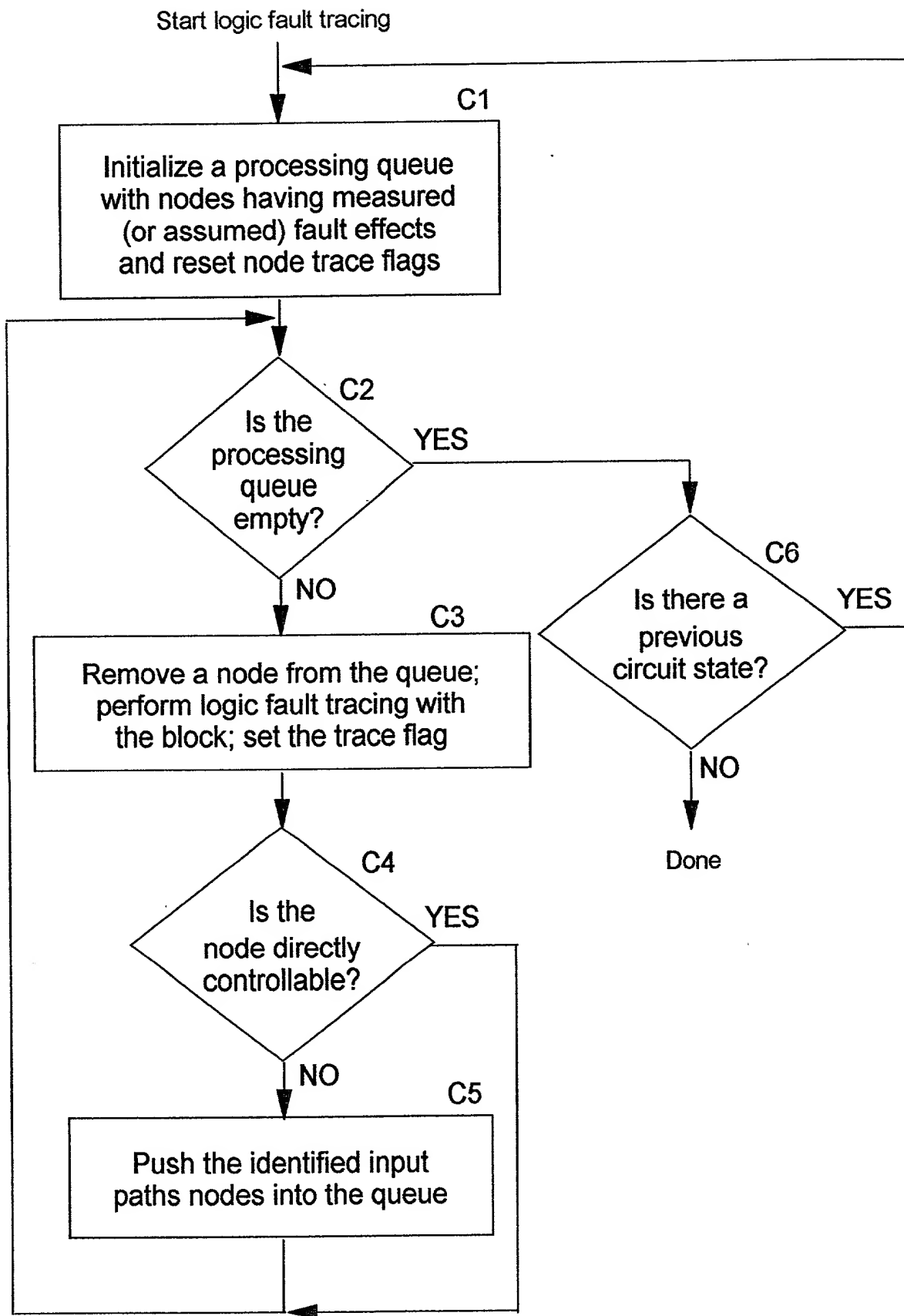


Figure 16.

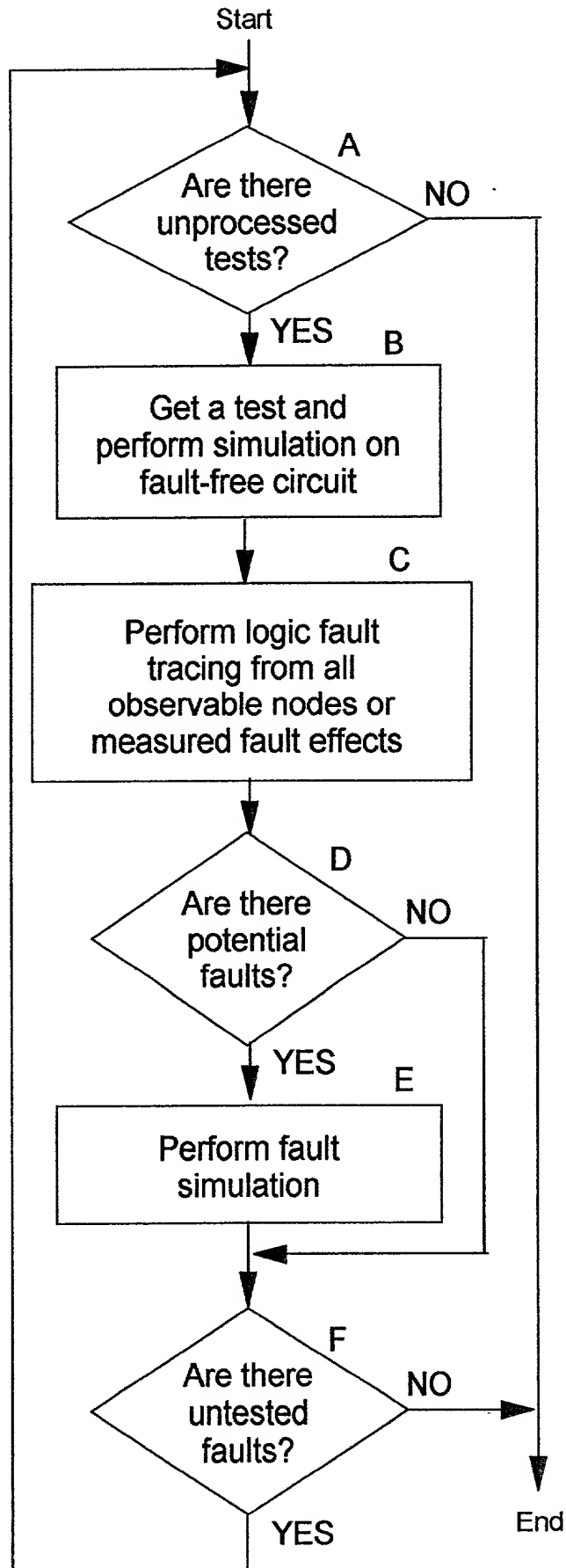


Figure 17.